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SCAN-MODE INDICATION TECHNIQUE FOR AN INTEGRATED CIRCUIT ABSTRACT OF THE DISCLOSURE

An integrated circuit and method for indicating the integrated circuit to enter into a scan mode are disclosed. A designated signal, such as an analog supply signal, for an analog block of an integrated circuit is utilized for indicating entry of a digital block of the integrated circuit into a scan mode.

Operations of the analog block and the digital block are generally independent from each other during scan mode. Prior to the digital block utilizing the designated signal, voltage rails for the designated signal are resolved with the voltage rails of a digital supply signal for the digital block.